

3W, Fixed input voltage, isolated & unregulated single output



## FEATURES

- Continuous short circuit protection
- Efficiency up to 85%
- Operating temperature range: -40°C to +85°C
- Isolation voltage: 1.5K VDC
- Miniature SMD package
- Internal surface mounted design
- International standard pin-out

*B\_0505T-3W series is designed for application where an isolated voltage is required in a distributed power supply system. It is suitable for*  
 1. *Where the voltage of the input power supply is stable (voltage variation:  $\pm 10\%$  Vin);*  
 2. *Where isolation is necessary between input and output (isolation voltage  $\leq 1500$  VDC);*  
 3. *Where do not has high requirement of line regulation, load regulation and the ripple & noise of the output voltage;*  
 Such as: pure digital circuits, low frequency analog circuits, relay-driven circuits.

## Selection Guide

Part No.	Input Voltage (VDC)	Output		Efficiency (% Min./Typ.) @ Full Load	Max. Capacitive Load ( $\mu$ F)
	Nominal (Range)	Output Voltage (VDC)	Output Current (mA)(Max./Min.)		
B0505T-3W	5 (4.5-5.5)	5	600/60	81/85	220

## Input Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Input Current (full load / no-load)		—	705/30	—	mA
Surge Voltage (1sec. max.)		-0.7	—	9	VDC
Reflected Ripple Current		—	25	—	mA
Input Filter		Capacitor filter			

## Output Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Output Voltage Accuracy	See tolerance envelope graph (Fig. 1)				
Line Regulation	Input voltage change: $\pm 1\%$	—	—	$\pm 1.2$	—
Load Regulation	10%-100% load	—	10	15	%
Ripple & Noise*	20MHz bandwidth	—	100	—	mVp-p
Temperature Drift Coefficient	100% load	—	—	$\pm 0.03$	%/°C
Output Short Circuit Protection	Continuous, self-recovery				

Note: \* Ripple and noise tested with "parallel cable" method, please see DC-DC Converter Application Notes for specific operation methods.

## General Specifications

Item	Operating Conditions	Min.	Typ.	Max.	Unit
Isolation Voltage	Input-output, with the test time of 1 minute and the leak current lower than 1mA	1500	—	—	VDC
Isolation Resistance	Input-output, isolation voltage 500VDC	1000	—	—	MΩ
Isolation Capacitance	Input-output, 100KHz/0.1V	—	17	—	pF
Operating Temperature	Derating if the temperature $\geq 71$ °C, (see Fig. 2)	-40	—	85	°C
Storage Temperature		-55	—	125	
Casing Temperature Rise	Ta=25 °C	—	25	—	
Pin Welding Resistance Temperature	Welding spot is 1.5mm away from the casing, 10 seconds	—	—	300	
Reflow Soldering Temperature	Peak temp. $\leq 240$ °C, maximum duration time $\leq 60$ s at 217 °C. For actual application, please refer to IPC/JEDEC J-STD-020D.1.				

Storage Humidity	Non-condensing	--	--	95	%
Switching Frequency	100% load, nominal input voltage	--	100	300	KHz
MTBF	MIL-HDFK-217F@25°C	3500	--	--	K hours

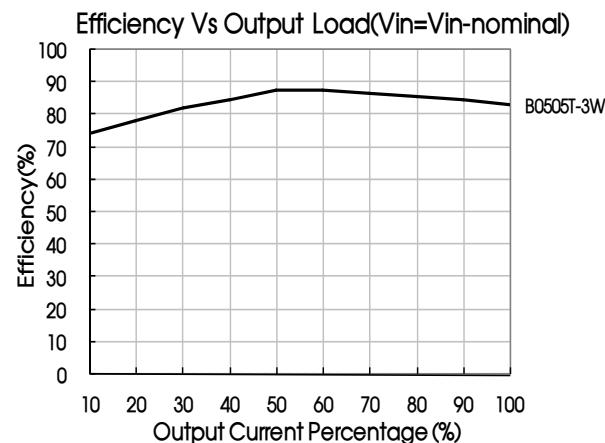
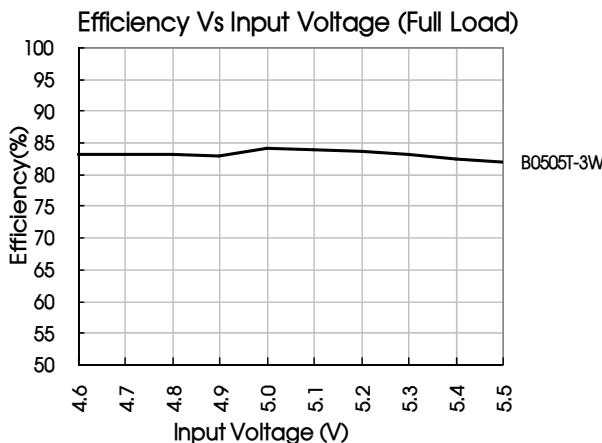
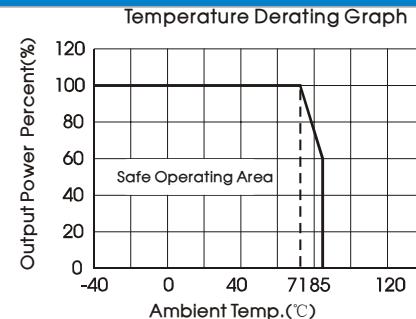
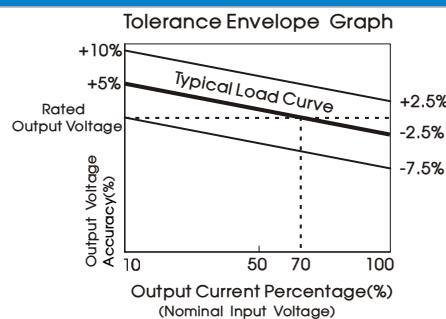
### Physical Specifications

Casing Material	Black flame-retardant heat-proof epoxy resin (UL94-V0)
Package Dimensions	19.00*10.46*6.60 mm
Weight	2.0g(Typ.)
Cooling Method	Free air convection

### EMC Specifications

EMI	Conducted disturbance	CISPR22/EN55022 CLASS B (see Fig. 5 for recommended circuit)
EMS	Electrostatic discharge	IEC/EN61000-4-2 Contact $\pm 6\text{KV}$ perf. Criteria B

### Product Characteristic Curve

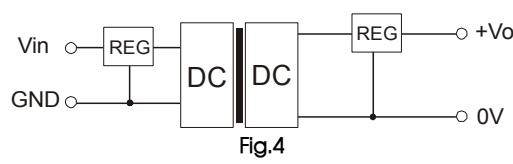
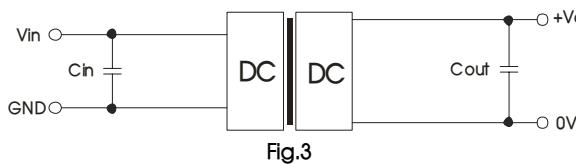


### Design Reference

#### 1. Typical application

If it is required to further reduce input and output ripple, a filter capacitor can be connected to the input and output terminals, see Fig.3. Moreover, choosing suitable filter capacitor is very important, start-up problems may be caused by too large capacitance. To ensure the modules running well, the recommended capacitive load values as shown in Table 1.

The simplest device for output voltage regulation, over-voltage and over-current protection is a linear voltage regulator with overheat protection that is connected to the input or output end in series (see Fig. 4).



Recommended capacitive load value table (Table 1)

Vin(VDC)	Cin(μF)	Vo (VDC)	Cout(μF)
5	4.7	5	10

It is not recommended to connect any external capacitor when output power is less than 0.5W.

2. EMC typical recommended circuit

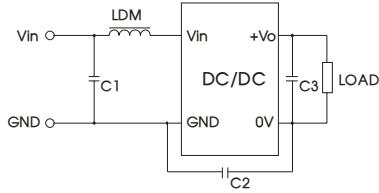


Fig. 5

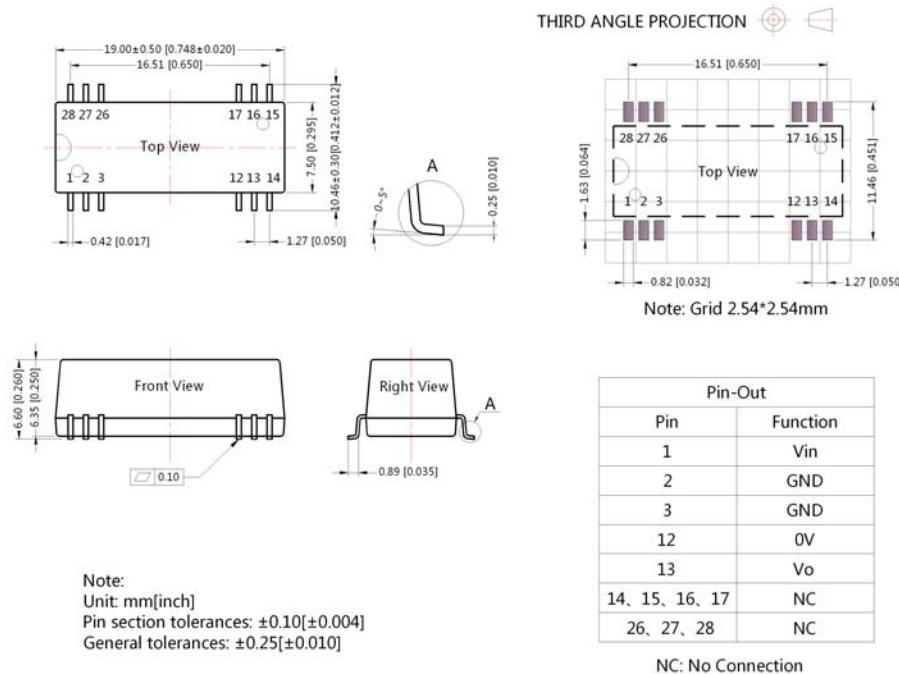
EMI	Input voltage (V)	5
	C1	4.7μF /50V
	C2	0.47nF/3KV
	C3	Refer to the Cout in Fig.3
	LDM	6.8μH

3. Output load requirements

To ensure the module work efficiently and reliably, during the operation, the min. output load should be no less than 10% of the full load. If the actual output power is low, please connect a resistor to the output terminal in parallel, with a recommended resistance which is 10% of the rated power, and derating is required during operation.

4. For more information please find the application notes on [1](#)

Dimensions and Recommended Layout



Notes:

1. Packing Information please refer to 'Product Packing Information'. Packing bag number: 58210024;
2. If the product is operated under the min. required load, the product performance cannot be guaranteed to comply with all performance indexes in this datasheet;
3. The max. capacitive load should be tested within the input voltage range and under full load conditions;
4. Unless otherwise specified, data in this datasheet should be tested under the conditions of  $T_a=25^{\circ}\text{C}$ , humidity<75% when inputting nominal voltage and outputting rated load;
5. All index testing methods in this datasheet are based on our Company's corporate standards;
6. The performance indexes of the product models listed in this manual are as above, but some indexes of non-standard model products will exceed the above-mentioned requirements, and please directly contact our technicians for specific information;
7. We can provide product customization service;
8. Specifications of this product are subject to changes without prior notice.